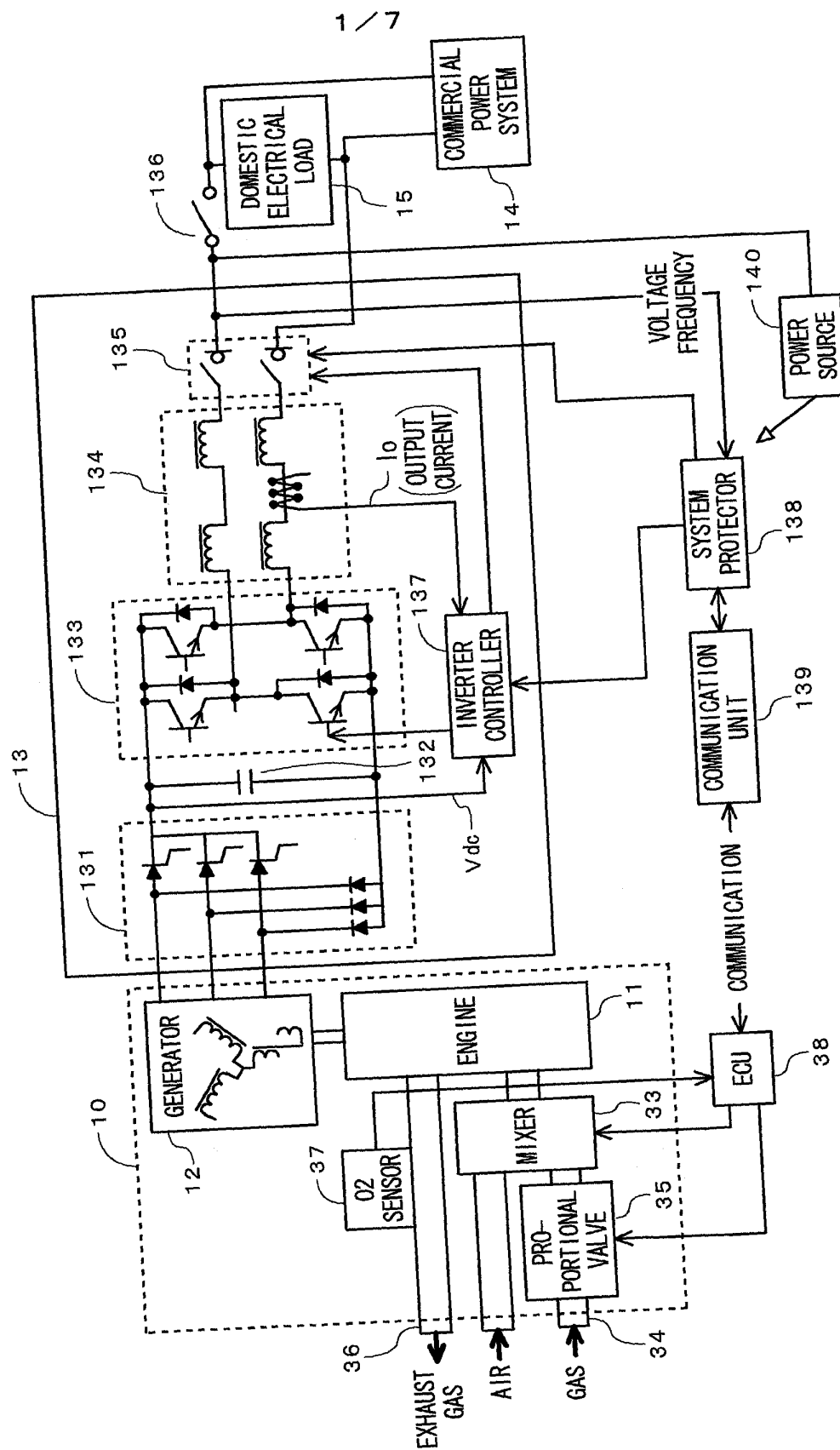
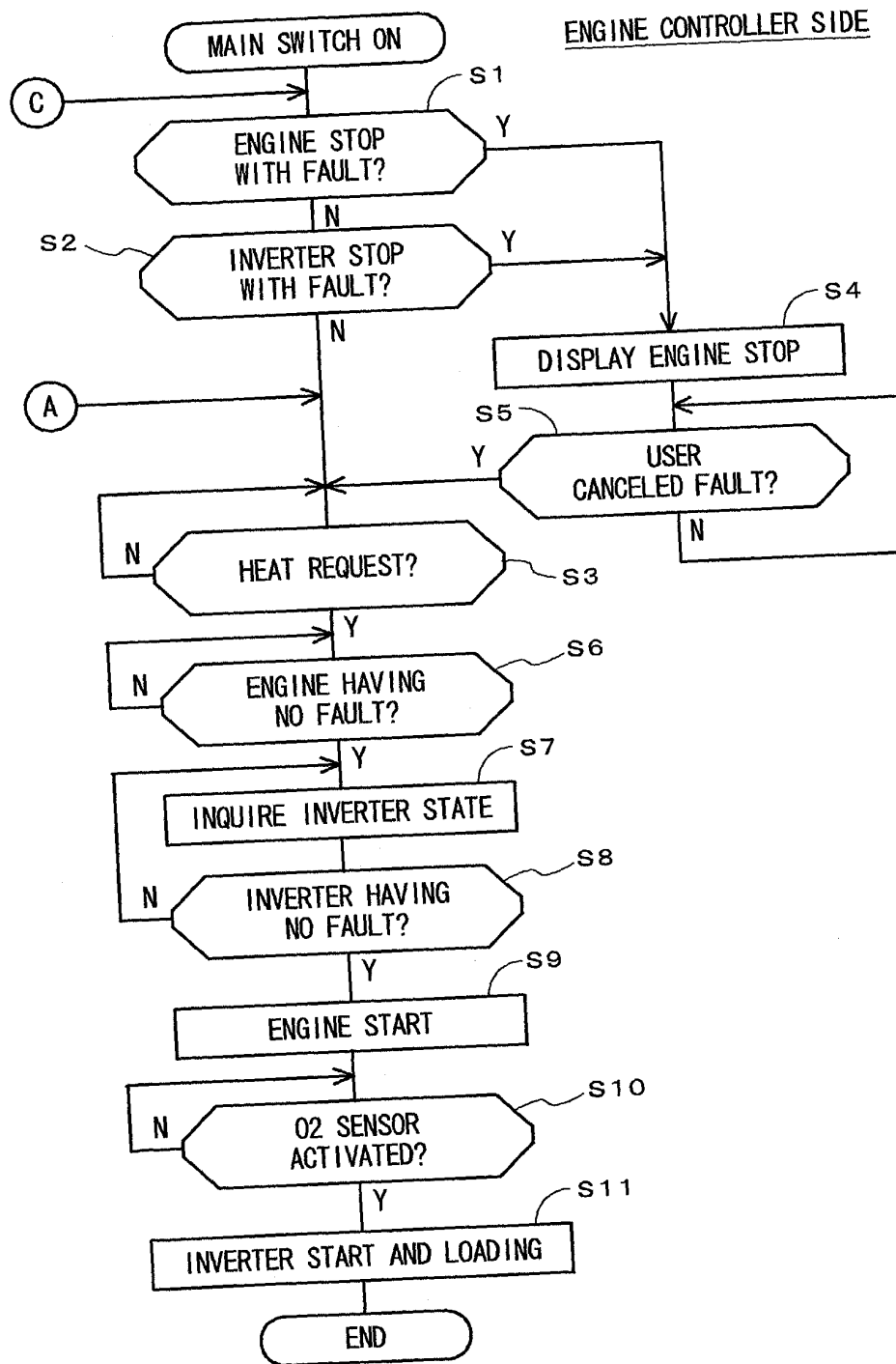


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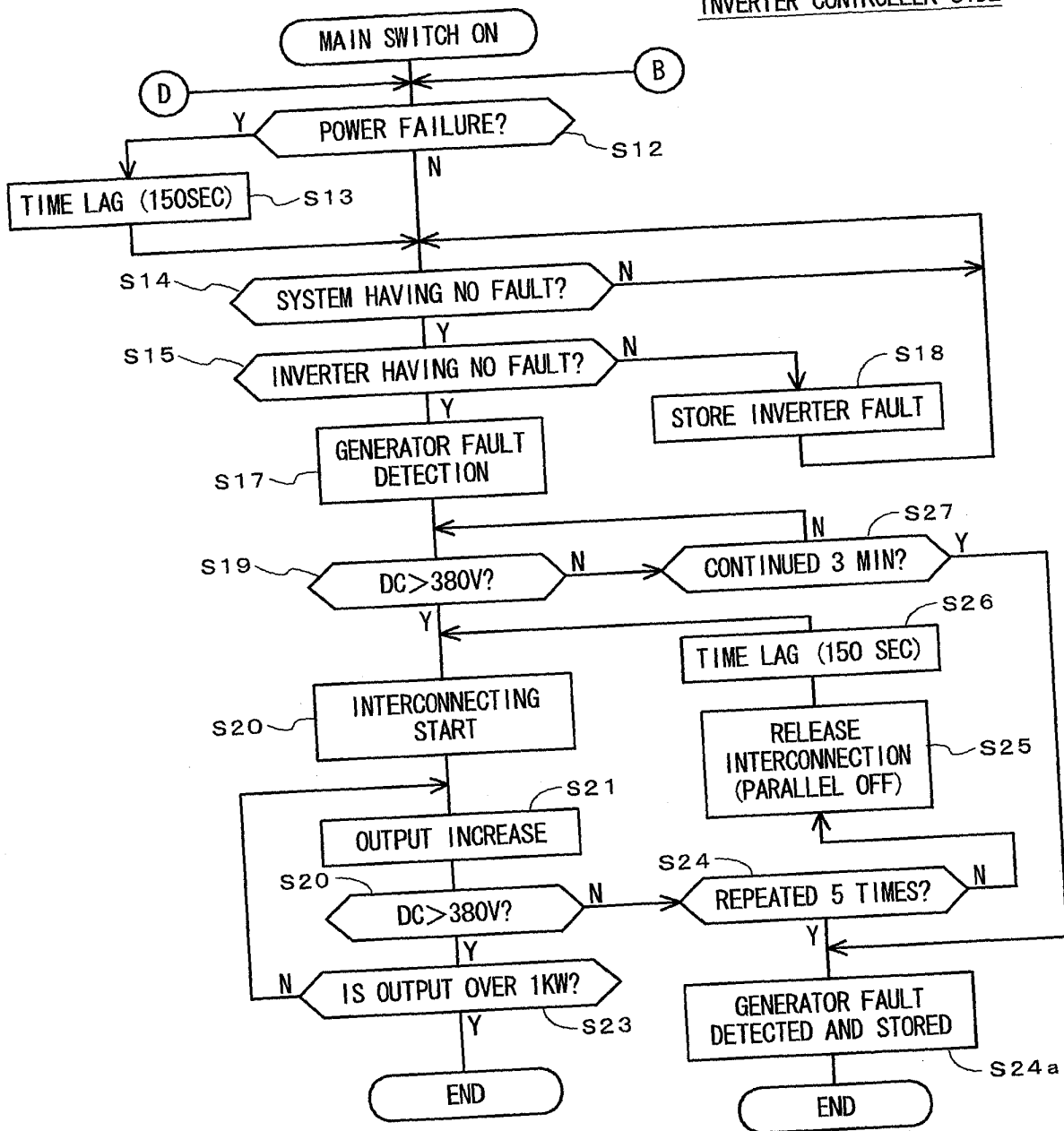
2 / 7
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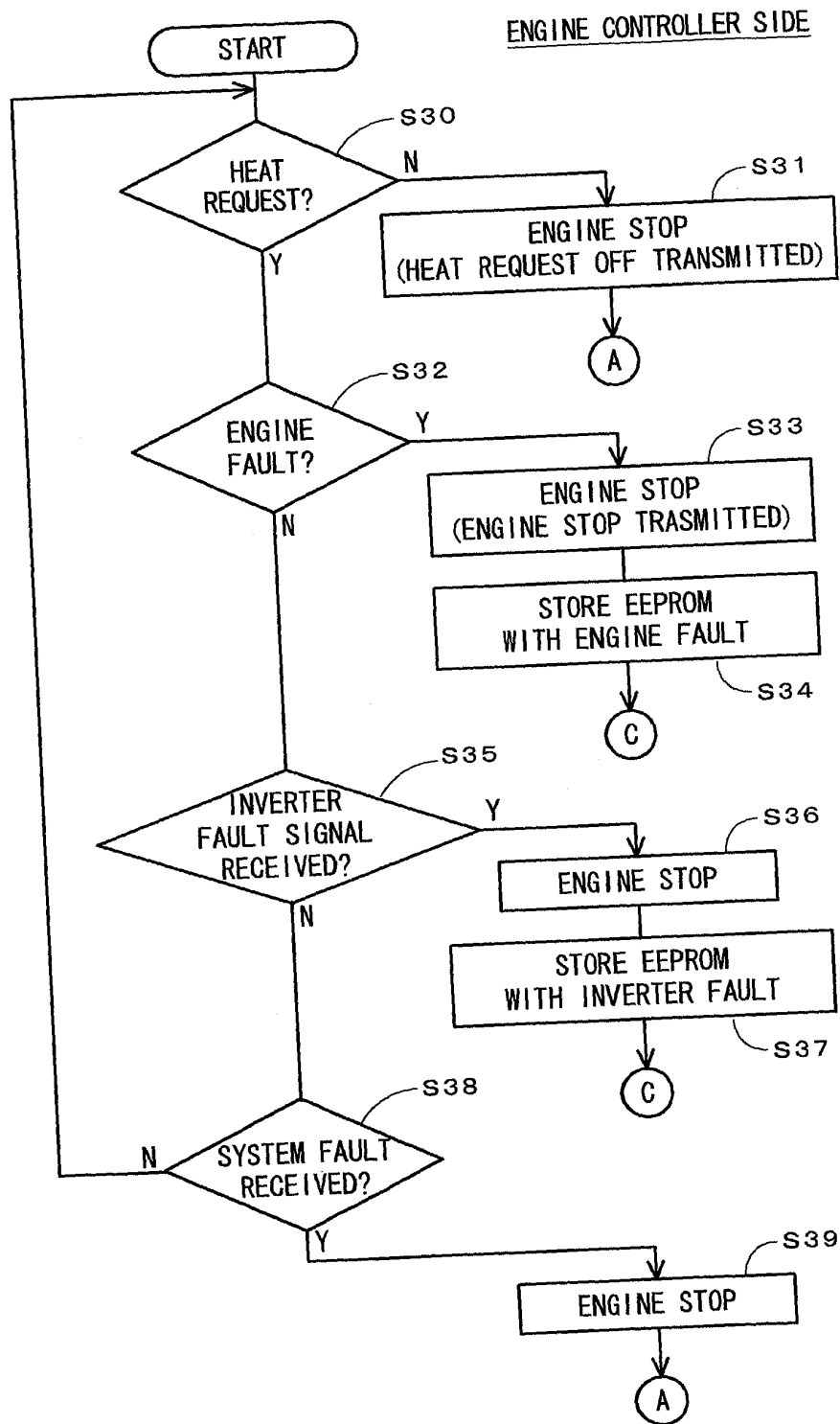
3 / 7
Fig. 3

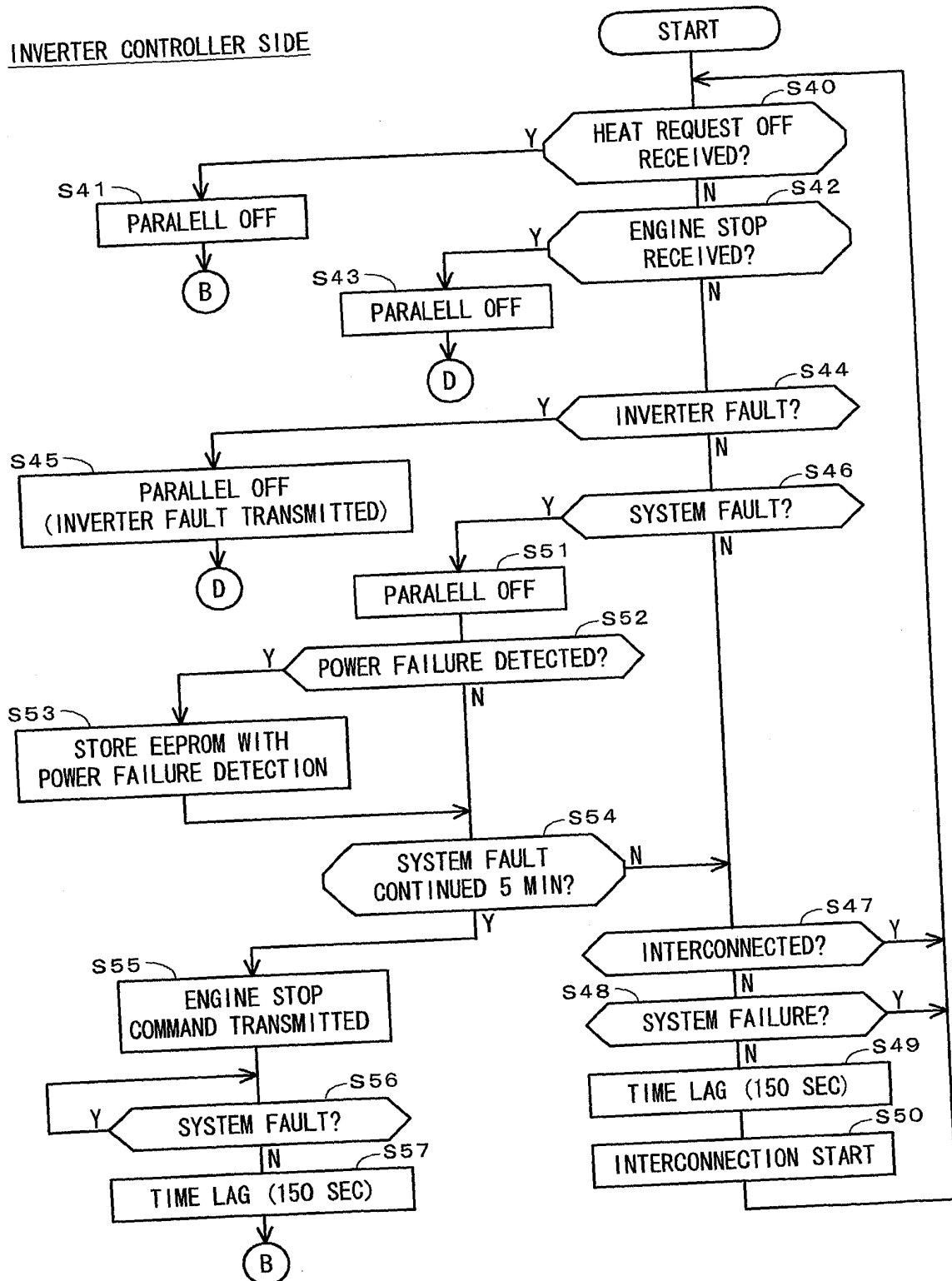
INVERTER CONTROLLER SIDE



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Fig. 4



[illegible]

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Fig. 7

